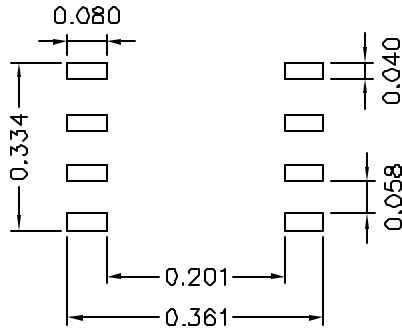
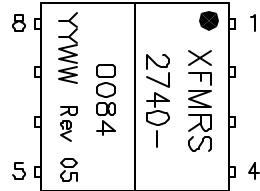
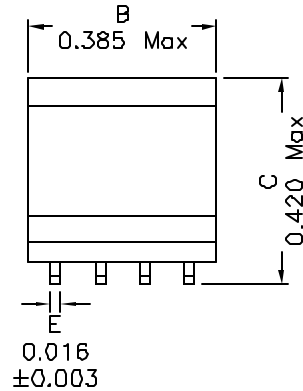
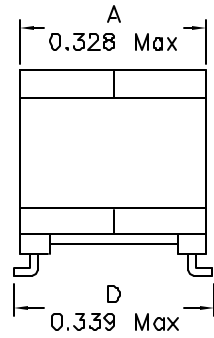
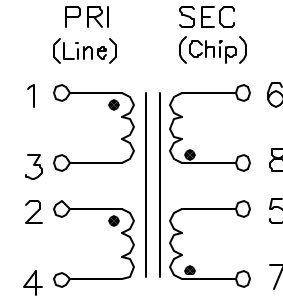


1. Mechanical Dimensions:



SUGGESTED PCB LAYOUT

2. Schematic:



3. Electrical Specifications: @25°C

- OCL: Pins 1-4 1.444mH±10% @10KHz 0.1V, Tie Pins 2&3
- LL: Pins 1-4 10uH Max @10KHz 0.1V, Tie Pins 2&3, Short SEC
- CW/W: Pins 1-8 60pF Max @10KHz 0.1V, Short 2-3 & 6-7
- THD: 80db Min @30KHz, $V_{rms}=4.05V/135 \text{ Ohms}$, Line:Chip
- THD: 75db Min @300KHz, $V_{rms}=4.05V/135 \text{ Ohms}$, Line:Chip
- Insertion Loss: Pins 1-4 0.5db Max @100KHz Line to Chip, Line Open
- Long Bal: 40dB Min 25KHz to 1.1MHz, Ground Pin 8
- DC Res.: Pins 1-3 2.0 Ohms Max
- DC Res.: Pins 2-4 2.0 Ohms Max
- DC Res.: Pins 8-6 0.700 Ohms Max
- DC Res.: Pins 7-5 0.700 Ohms Max
- Turns Ratio: (1-3):(2-4):(8-6):(7-5) = 1.31:1.31:1:1±2%
- Hipot: 1500Vac, Pri to SEC
- Hipot: 500Vac, Pri to Pri, Sec to Sec, All to Core

Notes:

1. Solderability: Leads shall meet MIL-STD-202, Method 208D for solderability.
2. Flammability: UL94V-0
3. ASTM oxygen index: > 28%
4. Insulation System: Class F 155°C. UL file E151556
5. Operating Temperature Range: -40°C to +85°C
6. Complies with IEC/EN60950 with Basic Insulation for working voltage of 250V.

Doc rev 05/1 indicates bobbin change.

DOC REV: 05/4

XFMRS Inc	Title: ADSL TRANSFORMER		
	UNLESS OTHERWISE SPECIFIED TOLERANCES: .xx ±0.010 Dimensions in Inch	P/N: 2740-0084	REV. 05
SHEET 1 OF 1	DWN.	Kang Chen	Feb-06-04
	CHK.	YK Liao	Feb-06-04
	APP.	Joe Huff	Feb-06-04